

## 400V High Voltage NPN Transistor

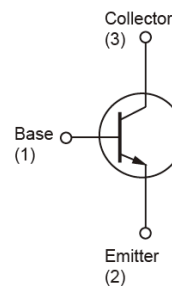
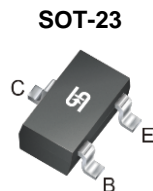
### FEATURES

- Epitaxial Planar Type
- NPN Silicon Transistor
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

### APPLICATION

- Consumer electronics
- High voltage switching
- High voltage driver

KEY PERFORMANCE PARAMETERS			
PARAMETER		VALUE	UNIT
BV <sub>CBO</sub>		400	V
BV <sub>CEO</sub>		400	V
I <sub>C</sub>		300	mA
V <sub>CE(SAT)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA	0.1	V



**Notes:** MSL 1 (Moisture Sensitivity Level) per J-STD-020

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Collector-Base Voltage	V <sub>CBO</sub>	400	V
Collector-Emitter Voltage	V <sub>CEO</sub>	400	V
Emitter-Base Voltage	V <sub>EBO</sub>	6	V
Collector Current (DC)	I <sub>C</sub>	300	mA
Power Total Dissipation @ T <sub>A</sub> =25°C	P <sub>D</sub>	0.225	W
Maximum Operating Junction Temperature	T <sub>J</sub>	+150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

**Note:** Single pulse, P<sub>w</sub> ≤ 380μs, Duty ≤ 2%

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	TYP	UNIT
Junction to Ambient Thermal Resistance	R <sub>θJA</sub>	556	°C/W
Junction to Case Thermal Resistance	R <sub>θJC</sub>	185	°C/W

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_A = 25^\circ\text{C}$ unless otherwise noted)						
<b>PARAMETER</b>	<b>CONDITIONS</b>	<b>SYMBOL</b>	<b>MIN</b>	<b>TYP</b>	<b>MAX</b>	<b>UNIT</b>
<b>Static</b> (Note 1)						
Collector-Base Breakdown Voltage	$I_C = 50\mu\text{A}, I_E = 0$	$BV_{CBO}$	400	--	--	V
Collector-Emitter Breakdown Voltage	$I_C = 1\text{mA}, I_B = 0$	$BV_{CEO}$	400	--	--	V
Emitter-Base Breakdown Voltage	$I_E = 50\mu\text{A}, I_C = 0$	$BV_{EBO}$	6	--	--	V
Collector Cutoff Current	$V_{CB} = 400\text{V}, I_E = 0$	$I_{CBO}$	--	--	10	$\mu\text{A}$
Collector-Emitter Reverse Current	$V_{CE} = 300\text{V}, R_{EB} = 4\text{k}\Omega$	$I_{CER}$	--	--	20	nA
Emitter Cutoff Current	$V_{EB} = 6\text{V}, I_C = 0$	$I_{EBO}$	--	--	10	$\mu\text{A}$
Collector-Emitter Saturation Voltage	$I_C = 10\text{mA}, I_B = 1\text{mA}$	$V_{CE(SAT)}$	--	0.1	0.5	V
Base-Emitter Saturation Voltage	$I_C = 10\text{mA}, I_B = 1\text{mA}$	$V_{BE(SAT)}$	--	--	1.5	V
DC Current Transfer Ratio	$V_{CE} = 10\text{V}, I_C = 10\text{mA}$	$h_{FE}$	100	--	270	
<b>Dynamic</b> (Note 2)						
Transition Frequency	$V_{CE} = 10\text{V}, I_C = -10\text{mA}, f = 10\text{MHz}$	$f_T$	--	20	--	MHz
Output Capacitance	$V_{CB} = 10\text{V}, I_E = 0, f = 1\text{MHz}$	$C_{ob}$	--	7	--	pF

**Note:**

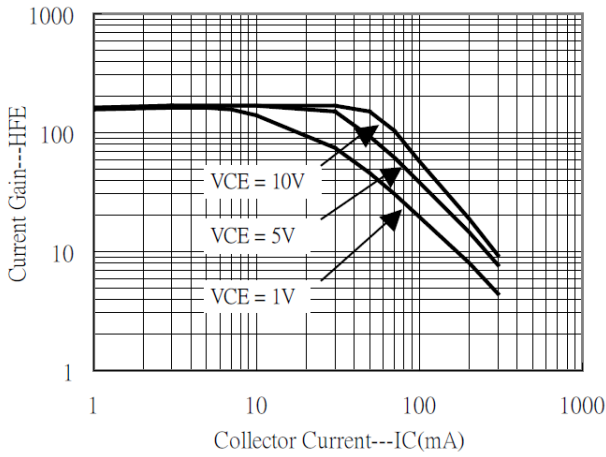
1. Pulse test:  $\leq 380\mu\text{s}$ , duty cycle  $\leq 2\%$
2. For DESIGN AID ONLY, not subject to production testing

**ORDERING INFORMATION**

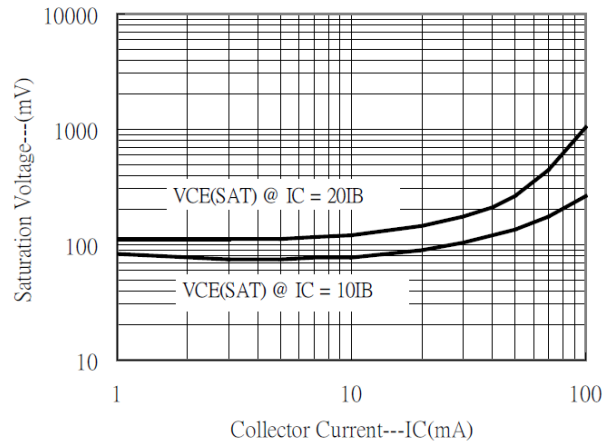
<b>ORDERING CODE</b>	<b>PACKAGE</b>	<b>PACKING</b>
TSC4505CX RFG	SOT-23	3,000pcs / 7" Reel

**ELECTRICAL CHARACTERISTICS CURVES** ( $T_A=25^\circ\text{C}$ , unless otherwise noted)

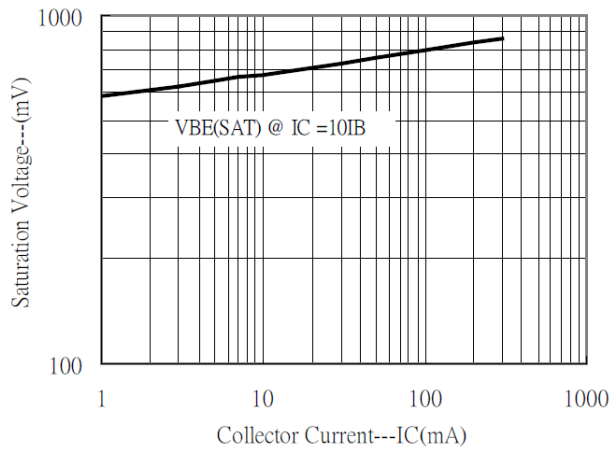
**Figure 1. DC Current Gain**



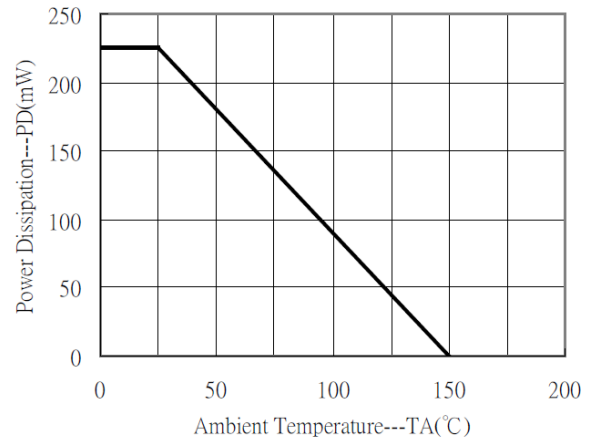
**Figure 2.  $V_{CE(SAT)}$  v.s.  $I_C$**



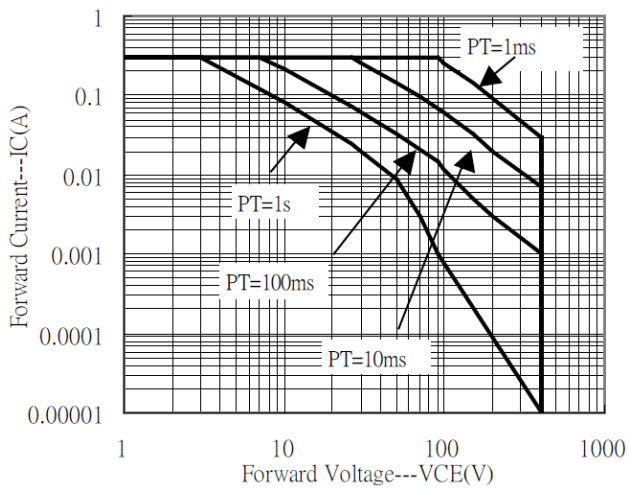
**Figure 3.  $V_{BE(SAT)}$  v.s.  $I_C$**



**Figure 4. Power Derating Curve**

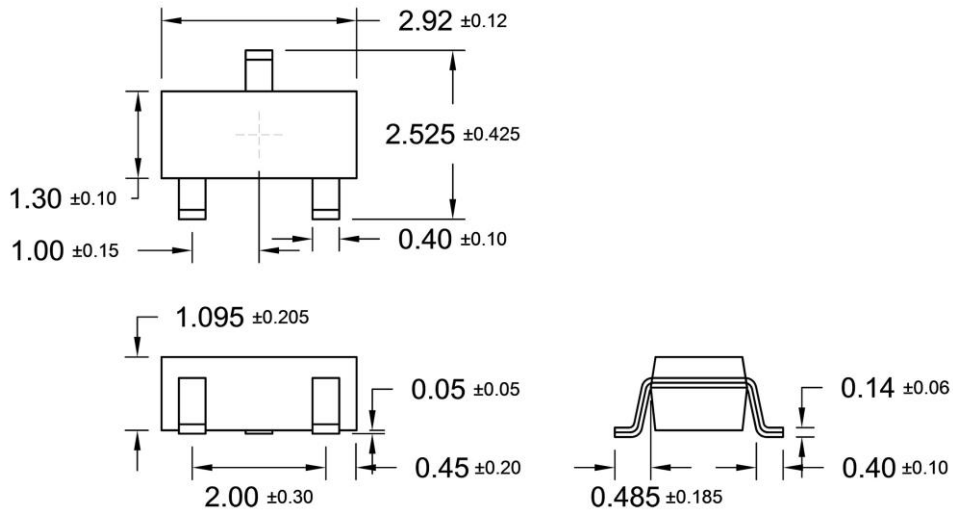


**Figure 5. Safe Operating Area**

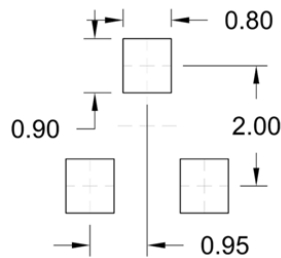


**PACKAGE OUTLINE DIMENSIONS** (Unit: Millimeters)

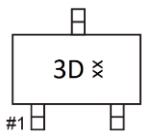
**SOT-23**



**SUGGESTED PAD LAYOUT** (Unit: Millimeters)



**MARKING DIAGRAM**



- 3D** = Device Code
- x** = Year Code  
7=2017, 8=2018, 9=2019, 0=2020.....
- x** = Month Code
 

<b>1</b> =Jan	<b>2</b> =Feb	<b>3</b> =Mar	<b>4</b> =Apr
<b>5</b> =May	<b>6</b> =Jun	<b>7</b> =Jul	<b>8</b> =Aug
<b>9</b> =Sep	<b>A</b> =Oct	<b>B</b> =Nov	<b>C</b> =Dec